Degree competences to which the subject contributes

Specific:
CEEIA-19. Understand the applications of electrical technology.
CEEIA-20. Understand the fundamentals and applications of analogue electronics.

Transversal:
05 TEQ N2. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.

Teaching methodology

The methodologies used for the development of the subject are as follows:
- Lecture with multimedia support, in order to provide information to the student so synthesized and organized.
- Class participatory exhibition, in which and in order that the student is not merely a passive element in the learning process, the teacher performs direct questions or debates on points considered particularly relevant or conceptual difficulty proposed.
- Problem-based learning, either individually or in a group in which the teacher proposes solving exercises outside the classroom so that the student can assess the degree of understanding of the subject.
- In the experimental laboratory sessions the methodology adopted is that of small cooperative groups in which students acquire skills in simulation techniques and testing of circuits.

Learning objectives of the subject

Acquire knowledge of the principles and techniques of circuit analysis, and be able to apply to the study of electrical and electronic circuits.
To acquire the knowledge to analyze time and frequency behavior of electronic circuits with different signals. Perform an introduction to basic electronic devices (diodes, transistor, operational amplifier), to common electronic circuits (amplifiers, filters, ...) and their associated models. Acquire basic knowledge of electrical machines and their application in electrical systems. Acquiring skills in experimental assay techniques circuits and electrical systems. Acquire knowledge in software tools of analysis and study of circuits.
As well:
Acquiring the ability to learn autonomously new concepts and techniques in the study and synthesis of circuits.
Acquiring the ability and commitment to organize group tasks.

<table>
<thead>
<tr>
<th>Study load</th>
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<tbody>
<tr>
<td><strong>Total learning time:</strong> 150h</td>
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<tr>
<td>Hours large group:</td>
<td>45h</td>
<td>30.00%</td>
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<tr>
<td>Hours medium group:</td>
<td>0h</td>
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<tr>
<td>Hours small group:</td>
<td>15h</td>
<td>10.00%</td>
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<tr>
<td>Guided activities:</td>
<td>0h</td>
<td>0.00%</td>
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<tr>
<td>Self study:</td>
<td>90h</td>
<td>60.00%</td>
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</table>
## Content

<table>
<thead>
<tr>
<th>Unit 01: Circuit analysis techniques.</th>
<th>Learning time: 17h</th>
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</thead>
<tbody>
<tr>
<td><strong>Description:</strong></td>
<td>Theory classes: 6h</td>
</tr>
<tr>
<td>Basics concepts: voltage, current,</td>
<td>Laboratory classes: 2h</td>
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<tr>
<td>resistance, conductance, Ohm's law,</td>
<td>Self study : 9h</td>
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<tr>
<td>power and energy.</td>
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<tr>
<td>Basics elements on electrical</td>
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<tr>
<td>circuits: voltage sources, current,</td>
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<tr>
<td>resistors.</td>
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<tr>
<td>Basic analysis techniques: Laws of</td>
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<tr>
<td>Kirchhoff, equivalent circuits,</td>
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<tr>
<td>voltage divider and current analysis</td>
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<tr>
<td>of branches, loops and knots.</td>
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<tr>
<td>Examples of application in electrical engineering.</td>
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<tr>
<td>Theorems and conversion circuits:</td>
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<tr>
<td>linearity, superposition theorem,</td>
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<tr>
<td>source transformation, Thevenin's</td>
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<tr>
<td>theorem, Norton theorem. Examples</td>
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<tr>
<td>of applications in electrical</td>
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<tr>
<td>engineering.</td>
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<tr>
<td>Interconnection between loads and</td>
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<td>generators: loading effects theorem</td>
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<tr>
<td>and maximum power transfer.</td>
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<tr>
<td>controlled sources (VCVS, CCVS,</td>
<td></td>
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<tr>
<td>VCCS and CCCS) and analysis.</td>
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<tr>
<td>Controlled source applications in</td>
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<tr>
<td>modeling operational amplifiers and</td>
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<tr>
<td>transistors: practical examples.</td>
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<tr>
<td>Important parameters of amplifier</td>
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<tr>
<td>stages based on op.amp. and</td>
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<tr>
<td>transistors: input impedance,</td>
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<tr>
<td>output impedance, gain,</td>
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<tr>
<td>bandwidth, etc.</td>
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<tr>
<td>Waveform generators.</td>
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</table>

<table>
<thead>
<tr>
<th>Unit 02: First and second order circuits.</th>
<th>Learning time: 19h</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Description:</strong></td>
<td>Theory classes: 6h</td>
</tr>
<tr>
<td>RC and RL circuits.</td>
<td>Laboratory classes: 4h</td>
</tr>
<tr>
<td>First-order circuits step response.</td>
<td>Self study : 9h</td>
</tr>
<tr>
<td>Initial and final conditions.</td>
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<tr>
<td>First-order circuit response to</td>
<td></td>
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<tr>
<td>exponential and sinusoidal inputs.</td>
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<tr>
<td>The series and parallel RLC circuit.</td>
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<tr>
<td>Second order circuit step response.</td>
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</tbody>
</table>
## Unit 03: Sinusoidal steady-state response. Phasors.

**Description:**
- Sinoidal excitation function. Phasor concept.
- Circuit theorems and analysis with phasors.
- Energy and power analysis.

**Related activities:**
- Problems collection
- Analysis and simulation of electric circuits by computer.

**Learning time:**
- Theory classes: 3h
- Laboratory classes: 0h
- Self study: 4h 30m

## Unit 04: Laplace transforms.

**Description:**
- Concepts and physical meaning.
- Signal waveforms and transforms.
- Basic properties.
- Pole-zero diagrams.
- Inverse Laplace transforms.
- Circuits response using Laplace transforms.
- 's' domain circuit analysis.
- Network functions and basic waveforms response.
- Impulse response and convolution.

**Related activities:**
- Problems collection
- Analysis and simulation of electric circuits by computer.

**Specific objectives:**
### Unit 05: Frequency response.

**Description:**
- Bode Diagrams.
- First-order low-pass and high-pass responses.
- Bandpass and bandstop responses.
- Others frequency responses in RLC circuits.
- Bode diagrams from poles and zeros.
- Frequency response and step response.
- Overview of Fourier analysis.
- Fourier coefficients.
- Waveform symmetries.
- Circuit analysis using the Fourier series.
- Fourier Transforms.
- Circuit analysis using Fourier transforms.

**Specific objectives:**

### Unit 06: AC power systems.

**Description:**
- Study of powers: average power, reactive power and complex power.
- Single-phase power circuits analysis in sinusoidal steady-state.
- Three-phase power circuits analysis in sinusoidal steady-state.

**Learning time:**
- 17h
  - Theory classes: 6h
  - Laboratory classes: 2h
  - Self study: 9h

**Learning time:**
- 7h 30m
  - Theory classes: 3h
  - Laboratory classes: 0h
  - Self study: 4h 30m
## Unit 07: Basic principles of electrical machines.

**Learning time:** 17h  
- Theory classes: 6h  
- Laboratory classes: 2h  
- Self study: 9h

**Description:**  
Revision of electrical machines.  
Transformers and magnetically coupled circuits: mutual inductance.  
DC machine: fundamentals, characteristics, mathematical analysis and applications.  
Different excitation systems on machine DC.

**Related activities:**

## Unit 08: AC electrical machines and others machines.

**Learning time:** 24h 30m  
- Theory classes: 9h  
- Laboratory classes: 2h  
- Self study: 13h 30m

**Description:**  
Rotating magnetic fields.  
AC asynchronous machine: fundamentals, characteristics, mathematical analysis and applications.  
Torque-speed characteristic, power and performance.  
Connection of three-phase motors.  
Operation of the single-phase motor.  
Special machines in Electronics and Automation Engineering: stepper motors, PMSM, servomotors, etc.  
Introduction to control: linear control, PWM control pulse control, etc.
Qualification system

The evaluation system consists on the following ratings with the partial weights:
- A Partial Test: 35%.
- A Final test: 35%.
- Laboratory: 10%.
- Monitoring exercises: 10%
- Competences: 10%

The partial test is a written test conducted in mid-course schedule.
The final test is performed when the classes are finished. The date is setup by academic organization.
The course grade (Nota_Curs) is obtained with the above weights:
Nota_Curs = Prova_Parcial*0.35 + Prova_Final*0.35 + Lab*0.1 + Ex*0.1 + Comp*0.1

A Reassessment Test, as written test of all course content, is contemplated for students whose course grade is suspended (Nota_Curs < 5.0). The date of the test is also established by academic organization.
The final grade (Nota_Curs) will be:
Nota_Curs = Prova_Reeval*0.7 + Lab*0.1 + Ex*0.1 + Comp*0.1
820225 - TCME - Circuit Theory and Electrical Machines

Bibliography

Basic:


Complementary:


Others resources:

Hyperlink

Apunts

Course notes