



Course guide

340100 - ELPO-E5010 - Power Electronics

Last modified: 17/05/2023

Unit in charge: Vilanova i la Geltrú School of Engineering
Teaching unit: 710 - EEL - Department of Electronic Engineering.

Degree: BACHELOR'S DEGREE IN ELECTRICAL ENGINEERING (Syllabus 2009). (Compulsory subject).
BACHELOR'S DEGREE IN MECHANICAL ENGINEERING (Syllabus 2009). (Optional subject).

Academic year: 2023 **ECTS Credits:** 6.0 **Languages:** Spanish

LECTURER

Coordinating lecturer: Miguel Castilla Fernández

Others: Miguel Castilla Fernández

DEGREE COMPETENCES TO WHICH THE SUBJECT CONTRIBUTES

Specific:

1. CE25. Applied knowledge to power electronics.

Transversal:

2. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.
3. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.
4. EFFECTIVE USE OF INFORMATION RESOURCES - Level 2. Designing and executing a good strategy for advanced searches using specialized information resources, once the various parts of an academic document have been identified and bibliographical references provided. Choosing suitable information based on its relevance and quality.
5. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

TEACHING METHODOLOGY

The course includes theory classes, which are oriented to introduce the initial knowledge, and exercises and laboratory classes, which are oriented to the application of the basic knowledge. The self study is guided by exercises and problems.

LEARNING OBJECTIVES OF THE SUBJECT

The course is oriented to the study of power electronics devices, the analysis and design of power electronics circuits, and the description of the main industrial applications. The objectives include: 1) to know the principles of power electronics, 2) to classify the different kind of power electronics circuits as a function of the input source and loads, 3) to analyze the basic power electronics converters, 3) to design the control circuits that command the basic power electronics converters.

STUDY LOAD

Type	Hours	Percentage
Hours small group	30,0	20.00
Self study	90,0	60.00
Hours large group	30,0	20.00

Total learning time: 150 h

CONTENTS

1. Introduction to Power Electronics

Description:

To present the basic principles of Power Electronics

Specific objectives:

To understand the features and differences between a signal processor circuit and a power processor circuit.

To define the types of power conversion according to the nature of input energy sources and output loads.

To identify the current and future industrial applications of power electronics converters.

Related activities:

Features of a power converter.

Classification of power converters.

Industrial applications of power converters.

Related competencies :

. CE25. Applied knowledge to power electronics.

03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 11h

Theory classes: 3h

Self study : 8h

2. Power Electronics Devices

Description:

To present the features of the most significant power electronics devices.

Specific objectives:

To understand the features of passive components and semiconductor devices.

To define the static parameters and the switching behaviour of power devices.

To understand the operation of activation and protection circuits for power devices.

Related activities:

Passive components.

Semiconductor devices.

Activation and protection circuits.

Related competencies :

. CE25. Applied knowledge to power electronics.

03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 20h

Theory classes: 6h

Self study : 14h



3. Rectifier Circuits

Description:

To present the basic configurations of rectifier circuits (single-phase, three-phase, circuits with diodes thyristors).
To analyze the properties of the circuits.

Specific objectives:

To understand the operation principle of the rectifier circuits.
To learn techniques to analyze rectifier circuits.
To understand the driving circuits of the controlled rectifiers

Related activities:

Exercices to analyze and design rectifier circuits.
Lab exercise. Measures and experimental verification. Report with the results and discussion.

Related competencies :

. CE25. Applied knowledge to power electronics.
07 AAT N2. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.
05 TEQ N2. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.
06 URI N2. EFFECTIVE USE OF INFORMATION RESOURCES - Level 2. Designing and executing a good strategy for advanced searches using specialized information resources, once the various parts of an academic document have been identified and bibliographical references provided. Choosing suitable information based on its relevance and quality.
03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 29h

Theory classes: 5h

Practical classes: 4h

Laboratory classes: 4h

Guided activities: 2h

Self study : 14h



4. Voltage Regulators

Description:

To present the basic configurations of dc-dc switching regulators.
To introduce the basic tools to analyze dc-dc switching regulators in steady-state.

Specific objectives:

To identify the configurations of dc-dc switching regulators.
To learn on techniques to analyze dc-dc- switching regulators in steady-state
To learn on programming tools to evaluate the dynamic performance of these converters
To learn how to design the control circuits

Related activities:

Exercises to analyze and design voltage regulators.
Lab exercise. Measures and experimental verification. Report with the results and discussion.

Related competencies :

. CE25. Applied knowledge to power electronics.
07 AAT N2. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.
05 TEQ N2. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.
06 URI N2. EFFECTIVE USE OF INFORMATION RESOURCES - Level 2. Designing and executing a good strategy for advanced searches using specialized information resources, once the various parts of an academic document have been identified and bibliographical references provided. Choosing suitable information based on its relevance and quality.
03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 36h

Theory classes: 6h
Practical classes: 4h
Laboratory classes: 4h
Guided activities: 2h
Self study : 20h



5. Power Inverters

Description:

To present the principle of operation of single-phase and three-phase power inverters and their associated modulation techniques. To analyze the properties of these circuits. To introduce the main applications of the power inverters.

Specific objectives:

To learn how to analyze and design power inverters.

To know how to reduce the harmonic content in voltage and current waveforms.

To learn on the techniques to derive dynamic models.

Related activities:

Exercises to analyze and design power inverters.

Lab exercise. Measures and experimental verification. Report with the results and discussion.

Related competencies :

. CE25. Applied knowledge to power electronics.

07 AAT N2. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.

05 TEQ N2. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.

06 URI N2. EFFECTIVE USE OF INFORMATION RESOURCES - Level 2. Designing and executing a good strategy for advanced searches using specialized information resources, once the various parts of an academic document have been identified and bibliographical references provided. Choosing suitable information based on its relevance and quality.

03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 32h

Theory classes: 6h

Practical classes: 4h

Laboratory classes: 4h

Guided activities: 2h

Self study : 16h



6. AC/AC Power Circuits

Description:

To present the basic configurations and applications of the AC/AC converters.

Specific objectives:

To know the analysis and design techniques for AC regulators, DC coupling converters, and cyclo-converters.

To know the properties of basic AC/AC circuits by experimental tests

Related activities:

Exercises to analyze and design AC/AC converters.

Lab exercise. Measures and experimental verification. Report with the results and discussion.

Related competencies :

. CE25. Applied knowledge to power electronics.

07 AAT N2. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.

05 TEQ N2. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.

06 URI N2. EFFECTIVE USE OF INFORMATION RESOURCES - Level 2. Designing and executing a good strategy for advanced searches using specialized information resources, once the various parts of an academic document have been identified and bibliographical references provided. Choosing suitable information based on its relevance and quality.

03 TLG. THIRD LANGUAGE. Learning a third language, preferably English, to a degree of oral and written fluency that fits in with the future needs of the graduates of each course.

Full-or-part-time: 22h

Theory classes: 4h

Practical classes: 3h

Laboratory classes: 3h

Self study : 12h

GRADING SYSTEM

The course is evaluated according to the following items:

- * Individual written exams (EXAM).
- * Laboratory classes (LAB).
- * Achievement of general and specific skills (SKIL).

The final mark (FM) is obtained using the following equation:

$$FM = 0.6 * EXAM + 0.3 * LAB + 0.1 * SKIL$$

The mark EXAM will be re-evaluable if NFIN is higher or equal to 3. In case of re-evaluation, the maximum value of NFIN will be 5.

EXAMINATION RULES.

None

BIBLIOGRAPHY

Basic:

- Kassakian, John G.; Schlecht, Martin F.; Verghese, George C. Principles of power electronics. Reading: Addison-Wesley, 1991. ISBN 0201096897.

- Mohan, Ned; Undeland, Tore M.; Robbins, William P. Power electronics : converters, applications, and design. 2003. New York [etc.]: John Wiley & Sons, 2003. ISBN 0471226939.