Master's degree in Electronic Engineering (MEE)

The master's degree in Electronic Engineering (download the MEE leaflet) caters for the needs of two types of students: those who wish to focus on a professional career and those looking to pursue a doctoral degree in Electronics Engineering.

This master’s degree provides graduates with a broad profile that includes skills and expertise in power, analogue and RF electronics, instrumentation and sensors, digital systems, micro and nanotechnologies, and microelectronics. After the first, compulsory subject area, students can choose from a wide variety of subjects in order to acquire a general profile, specialise in a field, or engage in research and pursue a doctoral degree.

The aim is for the graduates to enter modern industry as benchmark professionals in a new multidisciplinary work and production scenario. To increase their employability, students can take both the master’s thesis and some of the ECTS credits for optional subjects in a company or laboratory.

MEE has a strong international character. It is taught entirely in English and attracts a large number of students from other countries.

**GENERAL DETAILS**

**Duration and start date**
Two academic years, 120 ECTS credits. Starting September and February

**Timetable and delivery**
Afternoons. Face-to-face and blended learning

**Fees and grants**
Approximate fees for the master’s degree, excluding academic fees and degree certificate fee, €6,295 (€9,442 for non-EU residents).

**Scholarships for the degrees**
- **Fórum TIC**: 1 scholarship with a grant of €3,000.
- **HP**: 1 scholarship with a grant of €2,773,35, and an additional employment contract linked to the duration of the scholarship.
- **HP Academy**: 1 scholarship with a grant that covers the enrollment expenses of the master, with a work contract linked to the duration of the scholarship.

More information about grants and scholarships for the degree
More information about fees and payment options
More information about grants and loans

**Language of instruction**
English

**Location**
Barcelona School of Telecommunications Engineering (ETSETB)

**Official degree**
Recorded in the Ministry of Education's degree register

**ADMISSION**

**General requirements**
Academic requirements for admission to master's degrees
Specific requirements

This 120-credit master's programme consists of four semesters: bridging, core, specialisation and thesis. The syllabus of the first two semesters is compulsory, depending on the admission level. The syllabus of the specialisation semester is optional and focuses on both professional training and research. There are four specialisations: Power Electronics, Devices and Microsystems, Integrated Circuits and Systems, and Instrumentation and Measurements.

The bridging courses include subjects that some students will be required to take if they are not graduates of the bachelor’s degree in Electronic Systems Engineering or the equivalent from another university. The maximum number of ECTS credits that students are allowed to earn for bridging courses is 30 and these are included in the total of 120 ECTS credits for the master’s degree.

- Most students will have degrees in Telecommunications Engineering, Electronic Engineering, Electrical Engineering, Computer Engineering or Applied Physics. Students may be admitted to different semesters of the master’s programme on the basis of the subject of their degree and their academic CV.
- Potential master's degree candidates include both Spanish and foreign students with a bachelor's degree (a three- or four-year undergraduate university degree) in electrical engineering, computer engineering or applied physics. Until bachelor's programmes have been fully established, candidates may also come from pre-EHEA diploma programmes.
- Students with a bachelor's degree in Telecommunications or Electronic Engineering may be admitted to the core semester.
- Students admitted to the official postgraduate programme for the doctoral degree in Electronic Engineering who are required to take bridging courses may be admitted to the specialisation semester. Students from the pre-EHEA second cycle degree in Electronic Engineering who have completed all core and compulsory subjects may also be admitted to the specialisation semester.

Admission criteria

Language requirements:

CEFR English Level B2, which you can demonstrate in one of the following ways:

- Your mother tongue is English.
- You have studied in an English-speaking country (for at least one semester).
- You have taken an academic university programme taught in English (for at least one semester).
- You hold a European Higher Education Area degree that includes English Level B2.
- You hold one of the following English language certificates:
  - Cambridge: FCE
  - TOEFL PBT: $\geq 567$; CBT: $\geq 227$; IBT: $\geq 87$
  - IELTS: 5.5
  - TOEIC: 750
  - Escuela Oficial de Idiomas: Certificado de nivel avanzado (Level 5)
- You obtain a B2 English certificate at the UPC

Knowledge of Catalan and Spanish may be helpful for daily life. Find more information on the Language services and resources at the UPC website.

Places

40 in September + 20 in February

Pre-enrolment

Pre-enrolment period open.

How to pre-enrol

Enrolment

How to enrol

Legalisation of foreign documents

All documents issued in non-EU countries must be legalised and bear the corresponding apostille.
PROFESSIONAL OPPORTUNITIES

Professional opportunities
Given the cross-disciplinary nature of electronics, graduates of this degree may pursue careers in a broad range of sectors related to electronic technology, such as ICT systems, medical electronics, consumer electronics, control systems, robotics, automation, electromagnetic compatibility, microelectronic design, smart sensors and data acquisition systems.

Companies operating in these sectors offer high added value in terms of technology and are therefore in need of professionals trained to master's degree level. Many of these companies foster technology innovation and have a highly dynamic presence in a strongly competitive market, which they achieve through research.

Labour market
Every three years, the Catalan University Quality Assurance Agency (AQU) publishes a study on the employability of Catalan university graduates.
The last of these studies, Universities and Employment in Catalonia 2014, analyses the employability of students who graduated in the 2009-2010 academic year.
The most significant labour market data for electronic engineers are the following:

- The graduate employment rate is 97.4%.
- It takes 88.6% of graduates less than three months to find their first job.
- Of students who graduated in the 2009-2010 academic year, 90.3% earn over €2000 a month.
- Electronic engineering is in second place in the ranking of degree courses according to the Job Quality Index.

The Everis Foundation has issued a ranking of universities based on companies' views on the employability of new graduates. The UPC is the top Spanish university in the area of information and communication technologies (ICTs).

Competencies

Generic competencies
Generic competencies are the skills that graduates acquire regardless of the specific course or field of study. The generic competencies established by the UPC are capacity for innovation and entrepreneurship, sustainability and social commitment, knowledge of a foreign language (preferably English), teamwork and proper use of information resources.

Specific competences
On completion of the course, students will be able to:

- Model, design and control power electronic systems for various functions and applications.
- Conceive and design electronic circuits for RF analogue signal processing.
- Design, implement and integrate high-performance instrumentation systems.
- Analyse and design micro-and nanoelectronic devices within the margins of use.
- Analyse and design digital circuits and systems-based (multi-) processors and configurable devices.
- Analyse and design mixed-signal integrated circuits.
- Manage and generate innovative business projects in the field of electronic technology.

ORGANISATION

Organizing teaching center
- Technical School of Telecommunications Engineering of Barcelona (ETSETB)
CURRICULUM

MEE curriculum

Master MEE offers 2 types of academic paths:

- **Academic path without intensification:** If you want maximum flexibility in the elective subjects, choose this option. There are 45 compulsory ECTS credits and 45 ECTS to choose among the different elective options without any restriction. The master thesis has 30 ECTS.

- **Academic path with intensification:** If you want to be a specialist in one of the multiple areas of the electronic engineering, choose this option. There are 45 compulsory ECTS credits and among the 45 ECTS elective credits a minimum of 20 ECTS must be chosen from the intensification of your interest. The final thesis has 30 ECTS.

Subjects are structured in different blocks:

- **Bridge subjects:** To be taken by students whose academic profile is not a general bachelor of telecommunications engineering. The Academic Commission of Masters assigns these courses to new students. These subjects do not extend the master, they use elective credits.

- **Core subjects:** Compulsory subjects.

- **Elective subjects:** If the student does not want to follow any of the intensification tracks, any of the available elective subject can be chosen. In case of following an intensification track, the student must do a minimum of 20 ECTS of elective subjects from the chosen intensification track (**Energy Management, Integrated Circuits, Biomedical Engineering and Sensors, Micro and Nano Technologies**). If the student fulfills this requirement, the school will certify the followed intensification track.

- **Elective credits:** apart from elective subjects, these credits can be done with another activities like:
  - Introduction to Research subjects.
  - Seminars.
  - Internships in companies or laboratories (15 ECTS)
  - Recognized for professional experience in master's degree (15 ECTS maximum).

- **Master's Thesis.** The duration is 30 ECTS. You can check in [Departament d'Enginyeria Electrònica](https://www.upc.edu) the different research
groups where the student can develop his thesis.

**Master structure**

**Enrolment guide:**

**First semester (30 ECTS):** 6 core subjects. Any core subject except MTP. MTP has to be enrolled as late as possible.

**Second semester (30 ECTS):** The rest of core subjects except MTP + elective subjects. Without restrictions.

**Third semester (30 ECTS):** MTP + elective subjects. Without restrictions.

**Fourth semester (30 ECTS):** Master's thesis.

In case that bridge subjects are required, these will be enrolled in first and second semesters depending on the subject availability and the academic profile of each applicant.

Students can make mobility stays of half or full year to choose among a great number of foreign universities. Usually, the period is the third semester and/or the master's thesis during the fourth semester.

In case that the student is taking a double degree or a mobility stay in the second year, MTP must be passed during first and second semesters.

**Internships in companies:**

It is also possible to perform internships in companies. In the master's framework, these internships can be curricular equivalent to 15 elective ECTS, curricular to do the master's thesis or extracurricular (do not recognize credits). In the following link a list of companies that have received students in the last years can be found.

<table>
<thead>
<tr>
<th>Subjects</th>
<th>ECTS credits</th>
<th>Type</th>
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</thead>
<tbody>
<tr>
<td>COMPULSORY</td>
<td></td>
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<tr>
<td>Subjects</td>
<td>ECTS credits</td>
<td>Type</td>
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<tr>
<td>-------------------------------------------------------</td>
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<td>------------</td>
</tr>
<tr>
<td>Advanced Analog Circuit Techniques</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Advanced Digital Systems</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Innovation Based Service Management</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Instrumentation and Sensors</td>
<td>5</td>
<td>Compulsory</td>
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<tr>
<td>Management of Telecommunications Projects</td>
<td>5</td>
<td>Compulsory</td>
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<tr>
<td>Micro and Nano Electronic Design</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Micro and Nanotechnologies</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Power Control and Processing</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td>Signal Processing for Electronic Engineering</td>
<td>5</td>
<td>Compulsory</td>
</tr>
<tr>
<td><strong>OPTIONAL</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Advanced Analog System Design</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Advanced Control of Sensors and Actuators</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Analog and Mixed-Signal System-On-Chip Design</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Applied Convex Optimization</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Automotive Embedded Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Biomedical Instrumentation Design</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Building Your Career. From Academia to Startups &amp; Beyond</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Control Theory and Applications</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Critical Thinking and Creativity</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Custom Smart Adaptive Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Earth and Cosmos</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Edison: Energy Management for Distributed and Integrated Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Electric Motor Drives</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Electronic Devices Modelling</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Electronic Systems for Internet of Things</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Electronics for Communications Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Electronics Instrumentation Systems for Marine Applications</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Emc in Electronic Design</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Fabrication and Characterization Technologies for Micro and Nano Devices</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Fibers and Telecommunications</td>
<td>3</td>
<td>Optional</td>
</tr>
<tr>
<td>Financial Engineering: Applications to Information Technology Projects</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Fundamentals of Discrete-Time Signal Processing</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>GPS and Galileo Data Processing: From Fundamentals to High Accuracy Navigation</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Graphene and Carbon Nanotubes Introduction and Fundamentals</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Integrated Photonics</td>
<td>3</td>
<td>Optional</td>
</tr>
<tr>
<td>Introduction to Biomedical Electronic Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Introduction to Microelectronic Technologies</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Subjects</td>
<td>ECTS credits</td>
<td>Type</td>
</tr>
<tr>
<td>------------------------------------------------------------------------</td>
<td>--------------</td>
<td>--------</td>
</tr>
<tr>
<td>Introduction to Power Electronics</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Introduction to Research</td>
<td>15</td>
<td>Optional</td>
</tr>
<tr>
<td>Lidar Processing and Inversion: Applications to Remote Sensing of Physical Parameters</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Lidar Remote Sensing</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Marine Technology Instrumentation</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Matlab: Fundamentals And/Or Applications</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Mems. Microelectromechanical Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Microwave Circuits</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Microwave Photonics</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Mosic. Modelling, Simulation and Control of Power Electronic Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Optical Fiber Telecommunications</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Optoelectronics and Photovoltaic Technology</td>
<td>3</td>
<td>Optional</td>
</tr>
<tr>
<td>Photonics Systems in Telecommunications</td>
<td>3</td>
<td>Optional</td>
</tr>
<tr>
<td>Photovoltaic Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Power Control for Renewable Energy Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Power Electronic Circuits</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Printed Circuit Board Design</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Programmable Electronics</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Radiofrequency Integrated Circuits and Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Sensors, Instruments and Measurement Systems</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Software-Based Digital Control Applications</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Solar Cell Engineering</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Solar Cells for Dummies</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>System on Chip Physical Design</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Systems Based on Microprocessors</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Technology Asset Management</td>
<td>5</td>
<td>Optional</td>
</tr>
<tr>
<td>Telecommunications and Electronics Seminar</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>The Connected Vehicle</td>
<td>2.5</td>
<td>Optional</td>
</tr>
<tr>
<td>Ultrasonic Systems. Instrumentation and Applications</td>
<td>5</td>
<td>Optional</td>
</tr>
</tbody>
</table>

**PROJECT**

| Master's Thesis                                                       | 30           | Project |

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