230928 - TEL - Electronic Technology

Coordinating unit: 230 - ETSETB - Barcelona School of Telecommunications Engineering
Teaching unit: 710 - EEL - Department of Electronic Engineering
Academic year: 2019
Degree: BACHELOR'S DEGREE IN ELECTRONIC ENGINEERING AND TELECOMMUNICATION (Syllabus 2018). (Teaching unit Compulsory)
ECTS credits: 6
Teaching languages: Catalan, Spanish

Teaching staff

Coordinator: Jimenez Serres, Vicente
Others: Garcia Gonzalez, Miquel Angel

Prior skills


Degree competences to which the subject contributes

Specific:
CE8. (ENG) GREELEC: Capacitat per utilitzar eines informàtiques de cerca de recursos bibliogràfics o d'informació relacionada amb les telecomunicacions i l'electrònica. (Mòdul comú a la branca de telecomunicació).
CE29. (ENG) GREELEC: Capacitat d'analitzar i solucionar els problemes d'interferències i compatibilitat electromagnètica. (Mòdul de tecnologia específica- Sistemes electrònics).

Generical:
CG2. (ENG) GEELEC: coneixment, comprensió i capacitat per explicar la legislació necessària durant el desenvolupament de la professió d'enginyer tècnic de telecomunicació i facilitat per al maneig d'especificacions, reglaments i normes d'obligat compliment.
CG6. (ENG) GREELEC: Facilitat per al maneig d'especificacions, reglaments i normes d'obligat cumpliment.

Transversal:
CT5. (ENG) GREELEC: ÚS SOLVENT DELS RECURSOS DE LA INFORMACIÓ. Gestionar l'adquisició, l'estructuració, l'anàlisi i la visualització de dades i informació en l'àmbit de l'especialitat i valorar de forma crítica els resultats d'aquesta gestió.

Teaching methodology

Lectures
Laboratory sessions
Team assignments (at home)
Individual work
Continuous assessment evaluation
Final assessment evaluation

Learning objectives of the subject

Understand the task of the engineer in the development of an electronic product from the specifications to the final
product including the design of PCB circuits and the obligatory regulations. Understand the problems associated systems compatibility, both from the standardization and interference point of view. Understand the problems associated with the design of Printed Circuit Boards (PCBs) both at the level of its manufacturability and the problems associated with heat management or the integrity of the signals. Know the problems related to the safety of people and electronic equipment. Understand the current electronic equipment manufacturing process.

Study load

<table>
<thead>
<tr>
<th>Total learning time: 150h</th>
<th>Hours large group: 39h</th>
<th>26.00%</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Hours small group: 26h</td>
<td>17.33%</td>
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<tr>
<td></td>
<td>Self study: 85h</td>
<td>56.67%</td>
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</table>
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<table>
<thead>
<tr>
<th>Content</th>
<th>Learning time:</th>
</tr>
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<tbody>
<tr>
<td><strong>Topic 1: Introduction to the manufacture of electronic equipments</strong></td>
<td>5h</td>
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<tr>
<td><strong>Description:</strong></td>
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<tr>
<td>Typical elements within an electronic device: Analog and digital circuits, power supplies, cables, connectors, presentation elements and interaction, shielding, enclosure. Phases of the design of an electronic equipment.</td>
<td></td>
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<tr>
<td><strong>Learning time:</strong></td>
<td></td>
</tr>
<tr>
<td>Theory classes:</td>
<td>2h</td>
</tr>
<tr>
<td>Self study :</td>
<td>3h</td>
</tr>
</tbody>
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| **Topic 2: Printed Circuit Boards (PCB)**                               | 22h           |
| **Description:**                                                       |               |
| Elements of printed circuit boards. CAD tools for the design of PCBs. Stack-Up options. Phases of the design of a PCB. Management of the current return and grounding. Differential and controlled impedance lines. Thermal considerations. Standards and regulations. |               |
| **Learning time:**                                                     |               |
| Theory classes:                                                        | 10h           |
| Self study :                                                           | 12h           |

| **Topic 3: Electronic components technology.**                         | 19h           |
| **Description:**                                                       |               |
| Technological aspects of passive components, active devices and analog and digital integrated circuits. Limitations of components and non-idealities. |               |
| **Learning time:**                                                     |               |
| Theory classes:                                                        | 9h            |
| Self study :                                                           | 10h           |

| **Topic 4: Power subsystem**                                           | 7h            |
| **Description:**                                                       |               |
| **Learning time:**                                                     |               |
| Theory classes:                                                        | 3h            |
| Self study :                                                           | 4h            |
### Topic 5: Cables and connectors

**Description:**
Types of cables. Electrical shielding and susceptibility to magnetic fields. Dielectric effects. Connectors. Impedance matching.

**Learning time:** 7h
- Theory classes: 3h
- Self study: 4h

### Topic 6: Integration and assembly of electronic equipment

**Description:**
Integration of components. Automatic assembly. Implications in the design.

**Learning time:** 7h
- Theory classes: 3h
- Self study: 4h

### Topic 7: Electromagnetic compatibility concepts

**Description:**
Generation and susceptibility to electromagnetic radiation. Techniques to reduce emission and susceptibility. Compatibility regulations.

**Learning time:** 14h
- Theory classes: 6h
- Self study: 8h

### Topic 8: Security in electronic equipments

**Description:**

**Learning time:** 7h
- Theory classes: 3h
- Self study: 4h

### Laboratory: Design of a PCB

**Description:**

**Learning time:** 34h
- Theory classes: 20h
- Laboratory classes: 14h
Laboratory: Circuit measurements

Description:
Integrity tests for analog and digital signals. Noise, interference and crosstalk. Differential lines and single ended. Power integrity tests. Comparison with linear and commutated supplies. Effects of decoupling and PSRR.

Learning time: 28h
Theory classes: 16h
Laboratory classes: 12h

Qualification system
40% Final exam
35% Laboratory sessions
25% Continuous assessment evaluation

Bibliography

Basic:

Complementary: