Degree competences to which the subject contributes

Specific:
CT1.1A. To demonstrate knowledge and comprehension about the fundamentals of computer usage and programming, about operating systems, databases and, in general, about computer programs applicable to the engineering.
CT1.1B. To demonstrate knowledge and comprehension about the fundamentals of computer usage and programming. Knowledge about the structure, operation and interconnection of computer systems, and about the fundamentals of its programming.
CT3.6. To demonstrate knowledge about the ethical dimension of the company: in general, the social and corporative responsibility and, concretely, the civil and professional responsibilities of the informatics engineer.
CT5.2. To know, design and use efficiently the most adequate data types and data structures to solve a problem.
CT6.2. To demonstrate knowledge, comprehension and capacity to evaluate the structure and architecture of computers, and the basic components that compound them.
CT7.1. To demonstrate knowledge about metrics of quality and be able to use them.

Generic:
G2. SUSTAINABILITY AND SOCIAL COMPROMISE: to know and understand the complexity of the economic and social
270006 - EC - Computer Organization

phenomena typical of the welfare society. To be capable of analyse and evaluate the social and environmental impact.

Teaching methodology

Theory classes will combine lectures, in which the lecturer presents, explains and exemplifies the topics of study, with open discussion on issues raised and their advantages and disadvantages.

Problem-solving classes will be based on three different approaches: resolution by the lecturer commented on by students; resolution by students individually; and resolution by students in groups. Once students have completed problems, the lecturer will provide feedback on errors.

Laboratory classes will be similar to problem-solving classes, except that exercises will be worked on in pairs using semi-automatic correction tools. Laboratory exercises will be assessed on an ongoing basis to encourage diligence in students.

Learning objectives of the subject

1. Understand the hierarchical levels of a computer.
2. Understand the ISA of a standard RISC processor and the qualitative and quantitative performance implications of its design.
3. To know how to represent and perform operations with integers in various formats: two's complement, sign-magnitude and bias.
4. To know how to represent and perform operations with real numbers in IEEE 754 floating-point format.
5. Understand how data structured in vectors, matrices and tuples are stored and accessed.
6. To learn how to translate high-level programs to assembly language (and vice versa) using a standard ABI. The high-level elements to be translated are expressions, conditionals, loops and subroutines.
7. Design arithmetic units for multiplication and division operations with natural numbers.
8. Understand and manage the exception and interrupt concepts.
9. Understand the internal structure and operation of cache memory, especially aspects that affect system performance.
10. Understand the usefulness of virtual memory, its basic functioning and the support hardware required.
11. Understand compilation, linkage and loading processes in software development.
12. Meet task completion deadlines.
13. Understand the factors that affect microprocessor performance, consumption, sustainability and impact on the environment.

Study load

<table>
<thead>
<tr>
<th>Total learning time: 187h 30m</th>
<th>Hours large group: 45h 24.00%</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Hours medium group: 15h 8.00%</td>
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<tr>
<td></td>
<td>Hours small group: 15h 8.00%</td>
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<tr>
<td></td>
<td>Guided activities: 7h 30m 4.00%</td>
</tr>
<tr>
<td></td>
<td>Self study: 105h 56.00%</td>
</tr>
<tr>
<td>Item 1. Introduction</td>
<td></td>
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<tr>
<td>----------------------</td>
<td></td>
</tr>
<tr>
<td>Degree competences to which the content contributes:</td>
<td></td>
</tr>
<tr>
<td>Description:</td>
<td></td>
</tr>
<tr>
<td>Hierarchical description of the computer at different levels of abstraction. Performance metrics. Energy consumption metrics. Amdahl's law.</td>
<td></td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Item 2. Assembler and basic data types.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degree competences to which the content contributes:</td>
</tr>
<tr>
<td>Description:</td>
</tr>
<tr>
<td>Introduction to MIPS. Operands. Representation of natural numbers, integers and characters. Pointers, vectors and strings. Instruction formats.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Item 3. Program translation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degree competences to which the content contributes:</td>
</tr>
<tr>
<td>Description:</td>
</tr>
<tr>
<td>Logic operations and shifts. If and while statements. Subroutines. Compilation, linkage and loading.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Item 4. Arrays.</th>
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</thead>
<tbody>
<tr>
<td>Degree competences to which the content contributes:</td>
</tr>
<tr>
<td>Description:</td>
</tr>
<tr>
<td>Array storage. Sequential access to vectors and matrices.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Item 5. Integer and floating point arithmetic.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degree competences to which the content contributes:</td>
</tr>
<tr>
<td>Description:</td>
</tr>
<tr>
<td>Integers: addition, subtraction, multiplication and division. Floating point: representation, addition, multiplication and rounding.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Item 6. Cache Memory.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Degree competences to which the content contributes:</td>
</tr>
<tr>
<td>Description:</td>
</tr>
</tbody>
</table>
### Item 7. Virtual Memory.

**Degree competences to which the content contributes:**
- Motivation and terminology.
- Address translation.
- Handling misses.
- TLB.

**Description:**
Basic MIPS concepts and hardware support. Detailed functioning of an exception and model generic handling routine. Specific cases: TLB miss. System calls. Interrupts.

### Item 8. Exceptions / Interrupts.

**Degree competences to which the content contributes:**

**Description:**
Basic MIPS concepts and hardware support. Detailed functioning of an exception and model generic handling routine. Specific cases: TLB miss. System calls. Interrupts.
## Planning of activities

<table>
<thead>
<tr>
<th>Topic assimilation</th>
<th>Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Introduction</strong></td>
<td>13h</td>
</tr>
<tr>
<td><strong>Assemblers and basic data types</strong></td>
<td>17h</td>
</tr>
<tr>
<td><strong>Program translation</strong></td>
<td>26h</td>
</tr>
<tr>
<td><strong>Arrays</strong></td>
<td>12h</td>
</tr>
</tbody>
</table>

### Hours

- **Theory classes:**
  - Introduction: 3h 30m
  - Assemblers and basic data types: 4h
  - Program translation: 7h
  - Arrays: 2h

- **Practical classes:**
  - Introduction: 0h 30m
  - Assemblers and basic data types: 2h
  - Program translation: 3h
  - Arrays: 1h

- **Laboratory classes:**
  - Introduction: 2h
  - Assemblers and basic data types: 2h
  - Program translation: 3h
  - Arrays: 2h

- **Guided activities:**
  - Introduction: 0h
  - Assemblers and basic data types: 0h
  - Program translation: 0h
  - Arrays: 0h

- **Self study:**
  - Introduction: 7h
  - Assemblers and basic data types: 9h
  - Program translation: 13h
  - Arrays: 7h

### Description

- **Introduction**
  - Topic 1 activities
  - Specific objectives: 1, 13

- **Assemblers and basic data types**
  - Topic 2 activities
  - Specific objectives: 2, 3, 5, 6

- **Program translation**
  - Topic 3 activities
  - Specific objectives: 2, 6, 11
Description:
Topic 4 activities

Specific objectives:
2, 5, 6

**Topic assimilation: Integer and floating point arithmetic**

Description:
Topic 5 activities

Specific objectives:
4, 7

**Mid-semester exam (EP)**

Description:
This exam will reflect the learning objectives for the activities designed to facilitate assimilation of topics 1 to 5.

Specific objectives:
1, 2, 3, 4, 5, 6, 7, 11, 13

**Hours:** 28h
- Theory classes: 8h
- Practical classes: 3h
- Laboratory classes: 3h
- Guided activities: 0h
- Self study: 14h

**Topic assimilation: Cache memory**

Description:
Topic 6 activities

Specific objectives:
9

**Hours:** 28h
- Theory classes: 8h
- Practical classes: 3h
- Laboratory classes: 3h
- Guided activities: 0h
- Self study: 14h
| Topic assimilation: Virtual memory | Hours: 14h  
Theory classes: 4h  
Practical classes: 2h  
Laboratory classes: 0h  
Guided activities: 0h  
Self study: 8h |
|-----------------------------------|--------------------------------------------------|
| Description:  
Topic 7 activities |  |
| Specific objectives:  
10 |  |

| Topic assimilation: Exceptions and interrupts | Hours: 14h  
Theory classes: 4h  
Practical classes: 2h  
Laboratory classes: 0h  
Guided activities: 0h  
Self study: 8h |
|-----------------------------------------------|--------------------------------------------------|
| Description:  
Topic 8 activities |  |
| Specific objectives:  
8 |  |

| Analysis of the environmental implications of computer manufacture, use and recycling | Hours: 7h 30m  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 0h  
Guided activities: 0h 30m  
Self study: 7h |
|-------------------------------------------------------------------------------|--------------------------------------------------|
| Description:  
Task designed to evaluate the transferable competency in sustainability |  |
| Specific objectives:  
12, 13 |  |

| Consolidation of the learning objectives | Hours: 28h  
Theory classes: 0h  
Practical classes: 3h  
Laboratory classes: 0h  
Guided activities: 0h  
Self study: 25h |
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</tbody>
</table>
### Completion of assessment activities

<table>
<thead>
<tr>
<th>Description:</th>
<th>Hours: 7h</th>
</tr>
</thead>
<tbody>
<tr>
<td>Activities focused on an overall review of the subject</td>
<td>Theory classes: 0h</td>
</tr>
<tr>
<td></td>
<td>Practical classes: 0h</td>
</tr>
<tr>
<td></td>
<td>Laboratory classes: 0h</td>
</tr>
<tr>
<td></td>
<td>Guided activities: 7h</td>
</tr>
<tr>
<td></td>
<td>Self study: 0h</td>
</tr>
</tbody>
</table>

| Specific objectives: | 1, 2, 3, 4, 5, 6, 7, 8, 9, 10 |

### Laboratory exam (EL)

<table>
<thead>
<tr>
<th>Description:</th>
<th>Hours: 0h</th>
</tr>
</thead>
<tbody>
<tr>
<td>Activities related to assessment</td>
<td>Guided activities: 0h</td>
</tr>
<tr>
<td></td>
<td>Self study: 0h</td>
</tr>
</tbody>
</table>

| Specific objectives: | 12 |

### Final Exam (EF)

<table>
<thead>
<tr>
<th>Description:</th>
<th>Hours: 0h</th>
</tr>
</thead>
<tbody>
<tr>
<td>This exam will reflect the learning objectives for the activities designed to facilitate assimilation of topics 1 to 8.</td>
<td>Guided activities: 0h</td>
</tr>
<tr>
<td></td>
<td>Self study: 0h</td>
</tr>
</tbody>
</table>

| Specific objectives: | 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13 |
Qualification system

Marks will be awarded based on two components: the theory/problem-solving mark (NT) and the laboratory mark (NL), accounting for 80% and 20% of the final mark, respectively.

NT is obtained from a mid-semester exam (EP) mark, accounting for 20% of the total mark, and a final exam (EF) mark, accounting for 60% of the total mark. The EP mark can be recuperated through the EF mark, since the corresponding weight (20%) is applied to the maximum of the two marks.

NL is obtained from a laboratory exam (EP) and a continuous assessment (AC) mark, accounting for 85% and 15% of the NL, respectively. The AC mark is based on laboratory session tasks and the corresponding preparatory work.

The formula for calculating the final mark for the course is:

\[
\text{Mark} = \max(\text{EP} \times 0.20 + \text{EF} \times 0.60, \text{EF} \times 0.80) + (\text{EL} \times 0.85 + \text{AC} \times 0.15) \times 0.20
\]

Bibliography

Basic:


Others resources:

Hyperlink

http://docencia.ac.upc.edu/FIB/grau/EC