270013 - CI - Computer Interfacing

Coordinating unit: 270 - FIB - Barcelona School of Informatics
Teaching unit: 707 - ESALL - Department of Automatic Control
Academic year: 2019
Degree: BACHELOR'S DEGREE IN INFORMATICS ENGINEERING (Syllabus 2010). (Teaching unit Compulsory)
ECTS credits: 6  Teaching languages: Catalan, Spanish

Teaching staff

Coordinator: - Enric X. Martin Rull (enric.xavier.martin@upc.edu)
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         - Toni Benedico Blanes (toni.benedico@upc.edu)

Prior skills

Students are expected to be able to:
Program in a high-level language (preferably C).
Program in an assembly language.
Understand the functioning of different electronic components: R, L, C, diodes, MOS transistors.
Understand DC electronic circuits and voltage, current and consumption calculations.
Represent numbers in the binary and hexadecimal bases and performing arithmetic and logical operations on them.
Understand the functioning of the different logic gates and combinational and sequential blocks.
Understand how to analyse and synthesise logic circuits.
Understand processor structure and operation.
Understand the architecture and operation of a simple computer.
Understand computer memory operations and hierarchy.
Understand documents written in English.

Requirements

- Prerequisite EC
- Prerequisite F
- Prerequisite IC

Degree competences to which the subject contributes

Specific:
CT2.3. To design, develop, select and evaluate computer applications, systems and services and, at the same time, ensure its reliability, security and quality in function of ethical principles and the current legislation and normative.
CT2.5. To design and evaluate person-computer interfaces which guarantee the accessibility and usability of computer systems, services and applications.
CT4.1. To identify the most adequate algorithmic solutions to solve medium difficulty problems.
CT5.2. To know, design and use efficiently the most adequate data types and data structures to solve a problem.
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CT5.3. To design, write, test, refine, document and maintain code in an high level programming language to solve programming problems applying algorithmic schemas and using data structures.

CT5.6. To demonstrate knowledge and capacity to apply the fundamental principles and basic techniques of parallel, concurrent, distributed and real-time programming.

CT6.2. To demonstrate knowledge, comprehension and capacity to evaluate the structure and architecture of computers, and the basic components that compound them.

CT7.1. To demonstrate knowledge about metrics of quality and be able to use them.

CT7.2. To evaluate hardware/software systems in function of a determined criteria of quality.

CT7.3. To determine the factors that affect negatively the security and reliability of a hardware/software system, and minimize its effects.

CT8.1. To identify current and emerging technologies and evaluate if they are applicable, to satisfy the users needs.

CT8.4. To elaborate the list of technical conditions for a computers installation fulfilling all the current standards and normative.

**Generical:**

G3. THIRD LANGUAGE: to know the English language in a correct oral and written level, and accordingly to the needs of the graduates in Informatics Engineering. Capacity to work in a multidisciplinary group and in a multi-language environment and to communicate, orally and in a written way, knowledge, procedures, results and ideas related to the technical informatics engineer profession.

### Teaching methodology

No distinction is drawn between theory and problem-solving classes. Theory is reinforced with examples showing alternatives and solutions to interface problems.

Self-assessment exercises are proposed in the various topics so that students can assess their own progress. Students may consult the lecturer as necessary.

The practical sessions will take place in situ in the FIB teaching laboratory. An essential requirement for each practical is to have performed a pre-set task (to be specified).

### Learning objectives of the subject

1. Explain the various functions and define the main parameters of an I/O interface.
2. Describe the block configuration for various input/output subsystems.
3. Given the specifications for a particular microcomputer, program the various subsystems necessary to exchange data with the outside world and create and maintain programs that implement inputs and outputs using digital, analogue, pulse, parallel, serial, synchronous and asynchronous interfaces.
4. Identify the components and signals in different block diagrams for microcomputer architecture and indicate their use. They should be able to identify data and instruction paths and determine the value of the different registers involved in each execution phase for a given instruction.
5. Given a diagram for a simple electronic circuit connected to a specific microcomputer pin, quantify the different technological parameters (intensities, voltages, resistance, noise, maximums, etc.), identify possible sources of error and size the different components.
6. Quantify the resolution of an I/O operation and calculate quantification and sampling errors.
7. Program multiplexed input and output operations for a given interconnection diagram for a device with a microcomputer and calculate sampling frequencies.
8. Describe how to handle an interrupt from request to end of service and calculate, given a program and microcomputer specifications, the service time for an interrupt, latency time and the order in which different requests are served.
9. Program, given the specifications for a microcomputer and for all possible interrupt sources, service routines for the different interrupts while ensuring guaranteed time of service, program context recovery and restoration and identify critical regions.
10. Explain the characteristics of different types of storage, choose suitable storage for a specific context and measure width and capacity for different memories and the width of access buses.

11. Size the number of bits and work frequency for a timer and generate a signal of a specific frequency and duty cycle and lags for the desired duration. Students should also be able to accurately measure the period or frequency of an input signal, the instant when a pulse event occurs and the interval between two events.

12. Define and explain serial communication parameters, features and possible errors. Explain the differences in serial communication standards (UART, SPI, I2C, 1-Wire, CAN, etc.).

13. Describe the main features and functions of the USB bus and hub.

14. Describe the types and formats of USB packets, the packet transaction protocol in the presence and absence of errors, different types of endpoints and their performance in terms of speed, bus use, bandwidth warranty and error handling.

15. Locate a peripheral in the hierarchy of buses in the computer architecture.

16. Calculate the minimum expected transfer time between a memory and a device or between devices. They should also be able to draw the data path for different types of transfers and locate bottlenecks in multiple transfers between devices.

17. Quantify bus throughput at different levels (internal bus, local bus, system bus, expansion bus, peripheral buses) and explain the features of a bridge between buses.

18. Describe the basic I/O interconnection device configuration with DMA transfer and link DMA operational modes with the operations of different buses in the computer hierarchical structure. They should also be able to compare transfer time in a bus with and without DMA.

19. Correctly interpret technical descriptions, block diagrams, electronic diagrams and schedules in reference manuals and prepare the documentation necessary to transfer knowledge and ideas (block diagrams, electronic diagram, flowcharts, state diagrams, component lists, etc.).

20. Understand hardware in relation to the ins

### Study load

<table>
<thead>
<tr>
<th>Total learning time: 150h</th>
<th>Hours large group: 30h</th>
<th>20.00%</th>
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<tbody>
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<td>Hours medium group:</td>
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<tr>
<td>Hours small group:</td>
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<tr>
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<tr>
<td>Self study:</td>
<td>84h</td>
<td>56.00%</td>
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</table>
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Content

Introduction

Degree competences to which the content contributes:
Description:

Microcomputer architecture

Degree competences to which the content contributes:
Description:

Input/output ports

Degree competences to which the content contributes:
Description:

Interrupts

Degree competences to which the content contributes:
Description:

Impulse inputs and outputs

Degree competences to which the content contributes:
Description:
Programmable time controller diagram. Main time controller working modes. Time controllers to count asynchronous events. Generating output impulses using IT. Generating modulated signals in pulse width modulation (PWM) using IT.

Analogue Interfaces
### Serial communication interfaces

**Degree competences to which the content contributes:**

**Description:**

### Buses and DMA

**Degree competences to which the content contributes:**

**Description:**
## Planning of activities

<table>
<thead>
<tr>
<th>Development of item 1 of the course</th>
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<tbody>
<tr>
<td></td>
<td>Theory classes: 2h</td>
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<tr>
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<td>Practical classes: 0h</td>
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<td></td>
<td>Laboratory classes: 0h</td>
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<td>Guided activities: 0h</td>
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**Specific objectives:**
1, 2, 19

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<th>Development of item 2 of the course</th>
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<td>Guided activities: 0h</td>
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<td></td>
<td>Self study: 6h</td>
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</table>

**Specific objectives:**
1, 2, 3, 4, 6, 10, 16, 19

<table>
<thead>
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<tr>
<td></td>
<td>Guided activities: 0h</td>
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<tr>
<td></td>
<td>Self study: 6h</td>
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**Specific objectives:**
1, 2, 3, 4, 5, 7, 19, 20, 22

<table>
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<tr>
<td></td>
<td>Self study: 3h</td>
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**Specific objectives:**
4, 8, 9, 22
### Development of item 5 of the course

**Specific objectives:**
1, 2, 3, 4, 6, 11, 19, 22

**Hours:** 5h  
- Theory classes: 2h  
- Practical classes: 0h  
- Laboratory classes: 0h  
- Guided activities: 0h  
- Self study: 3h

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### Development of item 6 of the course

**Specific objectives:**
1, 2, 3, 4, 6, 7, 19

**Hours:** 5h  
- Theory classes: 2h  
- Practical classes: 0h  
- Laboratory classes: 0h  
- Guided activities: 0h  
- Self study: 3h

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### Development of item 7 of the subject

**Specific objectives:**
1, 2, 3, 4, 12, 13, 14, 19, 20

**Hours:** 15h  
- Theory classes: 6h  
- Practical classes: 0h  
- Laboratory classes: 0h  
- Guided activities: 0h  
- Self study: 9h

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### Development of the subject item 8

**Specific objectives:**
1, 2, 15, 16, 17, 18, 19, 21, 22

**Hours:** 10h  
- Theory classes: 4h  
- Practical classes: 0h  
- Laboratory classes: 0h  
- Guided activities: 0h  
- Self study: 6h
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<thead>
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<td><strong>Practice 2</strong></td>
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</table>
| Practice 5 | Hours: 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h |  
Specific objectives:  
3, 4, 5, 7, 19, 20, 21, 22, 23 |
|---|---|
| Practice 6 | Hours: 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h |  
Specific objectives:  
3, 4, 9, 19, 20, 21, 22, 23 |
| Lab 7 | Hours: 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h |  
Specific objectives:  
3, 4, 6, 11, 19, 20, 21, 22, 23 |
| Practice 8 | Hours: 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h |  
Specific objectives:  
3, 4, 6, 11, 19, 20, 21, 22, 23 |
### Practice 9

**Hours:** 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h

**Specific objectives:**  
3, 4, 5, 6, 7, 19, 20, 21, 22, 23

### Practice 10

**Hours:** 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h

**Specific objectives:**  
3, 4, 5, 12, 19, 20, 21, 22, 23

### Practice 11

**Hours:** 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h

**Specific objectives:**  
3, 4, 12, 13, 14, 19, 20, 21, 22, 23

### Practice 12

**Hours:** 5h  
Theory classes: 0h  
Practical classes: 0h  
Laboratory classes: 2h  
Guided activities: 0h  
Self study: 3h

**Specific objectives:**  
3, 4, 12, 13, 14, 19, 20, 21, 22, 23
# Practice 13

**Specific objectives:**
3, 4, 12, 19, 20, 21, 22, 23

**Hours:** 5h
- Theory classes: 0h
- Practical classes: 0h
- Laboratory classes: 2h
- Guided activities: 0h
- Self study: 3h

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# Practice 14

**Specific objectives:**
3, 4, 5, 7, 19, 20, 21, 22, 23

**Hours:** 0h
- Theory classes: 0h
- Practical classes: 0h
- Laboratory classes: 0h
- Guided activities: 0h
- Self study: 0h

---

# First partial test

**Specific objectives:**
1, 2, 3, 4, 5, 6, 7, 10, 19, 20, 21

**Hours:** 9h 30m
- Guided activities: 1h 30m
- Self study: 8h

---

# Second partial test

**Specific objectives:**
2, 3, 4, 6, 7, 8, 9, 11, 19, 20, 21, 22

**Hours:** 9h 30m
- Guided activities: 1h 30m
- Self study: 8h

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# Third partial test

**Specific objectives:**
2, 3, 4, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22

**Hours:** 9h 30m
- Guided activities: 1h 30m
- Self study: 8h
### Practical implementation of a microcomputer

**Specific objectives:**
3, 4, 5, 19, 20, 21, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h

### Practice I/O Ports

**Specific objectives:**
3, 4, 5, 19, 20, 21, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h

### Practice the 7 segment

**Specific objectives:**
3, 4, 19, 20, 21, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h

### Keyboard Practice

**Specific objectives:**
3, 4, 5, 6, 7, 19, 20, 21, 22, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h

### Practice interruptions

**Specific objectives:**
3, 4, 8, 9, 19, 20, 21, 22, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h

### Timers Practice

**Specific objectives:**
3, 4, 6, 9, 11, 19, 21, 22, 23

**Hours:** 0h
- Guided activities: 0h
- Self study: 0h
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<th>Activity</th>
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<th>Hours:</th>
<th>Guided activities:</th>
<th>Self study:</th>
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<td>Practice converter A / D</td>
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<tr>
<td>Practice serial com</td>
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<td>Practice mouse</td>
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<td>Practice USB</td>
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<td>Practice 1Wire</td>
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</table>
Practice touchscreen and GLCD

<table>
<thead>
<tr>
<th>Specific objectives:</th>
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<tbody>
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<td>3, 4, 5, 6, 7, 9, 19, 20, 21, 22, 23</td>
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<thead>
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<th>Hours:</th>
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Guided activities: 0h  
Self study: 0h  

Final Exam

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<thead>
<tr>
<th>Hours:</th>
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<tbody>
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</table>

Guided activities: 0h  
Self study: 0h  

Description:
Only those students who have requested within the deadline

Examination practice

<table>
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<tbody>
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<table>
<thead>
<tr>
<th>Hours:</th>
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<tr>
<td>0h</td>
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</tbody>
</table>

Guided activities: 0h  
Self study: 0h  

Qualification system

* During the course will take a minimum of 3 written tests corresponding to different parts of the course. Be made individually. Obtained a note (NT) from the average of the assessments.

* The grade laboratory NL obtained from the average of the individual assessments of practices. There will be between 10 and 14 evaluable practices during the year. The written tests will contain lab questions that will help to individualise the marks.

Students repeat the practices that are approved to be validated practices NL = 5.

* The final grade for the course comes from:

\[ \text{NF} = 0.65 \text{NT} + 0.35 \text{ NL} \]

* It is a necessary condition for passing the subject and presented properly perform laboratory practices.
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Bibliography

**Basic:**
