320024 - PEP - Power Electronics Processing

Coordinating unit: 205 - ESEIAAT - Terrassa School of Industrial, Aerospace and Audiovisual Engineering
Teaching unit: 709 - EE - Department of Electrical Engineering
Academic year: 2019
Degree: BACHELOR'S DEGREE IN ELECTRICAL ENGINEERING (Syllabus 2009). (Teaching unit Compulsory)
ECTS credits: 6

Teaching languages: Catalan, Spanish, English

Teaching staff

Coordinator: Joan Rocabert

Prior skills

Students will be expected to have passed Circuit Expansion, Electrical Systems and Electronic Systems, since the knowledge acquired in those subjects is the foundation on which an understanding of power electronic processes must be built.

Degree competences to which the subject contributes

Specific:
1. ELE: understanding of electrical power systems and their applications.

Transversal:
2. SELF-DIRECTED LEARNING - Level 3. Applying the knowledge gained in completing a task according to its relevance and importance. Deciding how to carry out a task, the amount of time to be devoted to it and the most suitable information sources.
3. TEAMWORK - Level 3. Managing and making work groups effective. Resolving possible conflicts, valuing working with others, assessing the effectiveness of a team and presenting the final results.

Teaching methodology

- Face-to-face sessions in which the lecturer will present concepts, give guidance to the group and set assignments.
- Applied face-to-face sessions. In these sessions, groups of four students will be required to hand in completed assignments to the lecturer. The lecturer decides which students will participate in each of these sessions.
- Independent learning. Students will be expected to use this time to learn concepts, complete the set assignments and prepare class work.
- Group work. Working in groups of four, students will be expected to complete practicals, write up post-practical reports and prepare for defence sessions.

Learning objectives of the subject

This subject introduces students to the analysis and design of power electronic processors. We start by reviewing the various types of power electronic devices before going on to study their basic configurations and most important industrial applications.

Students will gain an understanding of the operation and characteristics of the specific electronic and electromagnetic components used in power electronic processes.

They will become familiar with the operation and control systems of the most common power electronic processors (rectifiers, DC-DC and one-way power converters) and their use in DC and AC power systems.
Specifically, the subject is an introduction to the field traditionally known as power electronics, with the appropriate descriptive corrections.

<table>
<thead>
<tr>
<th>Study load</th>
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<tbody>
<tr>
<td><strong>Total learning time:</strong> 150h</td>
<td>Hours large group:</td>
<td>30h</td>
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<td></td>
<td>Hours medium group:</td>
<td>15h</td>
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<td>Hours small group:</td>
<td>15h</td>
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<td></td>
<td>Guided activities:</td>
<td>6h</td>
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<td></td>
<td>Self study:</td>
<td>84h</td>
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# TOPIC 1: INTRODUCTION

**Description:**
- Introduction to the subject
- Introduction to power electronic processing.

**Laboratory sessions**
Practical P0. Laboratory safety and rules for power electronic processing.

**Specific objectives:**
- A better understanding of the basic concepts of electrical systems.
- An understanding of the operating principle of power electronic processors.
- Familiarity with the behaviour guidelines for a power electronic processing laboratory.

## Learning time:
- Theory classes: 2h
- Laboratory classes: 1h

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# TOPIC 2: POWER SEMICONDUCTOR DEVICES

**Description:**
- Power diodes.
- Rectifiers.
- Power switches.
- Switching losses.
- Power switch technology.
- Trigger circuits.

**Laboratory sessions**
Practical P1. Power switching.

**Specific objectives:**
- A better understanding of the basic concepts of electrical devices.
- An understanding of the operation and peculiarities of power electronic devices.
- An understanding of the state of the art in power electronic devices.
- Familiarity with a tool for stimulating power electronic systems.
- Familiarity with power switch characteristics.

## Learning time:
- Theory classes: 4h
- Laboratory classes: 2h
## TOPIC 3: DIODE RECTIFIERS

<table>
<thead>
<tr>
<th>Learning time: 10h</th>
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<tbody>
<tr>
<td>Theory classes: 6h</td>
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<tr>
<td>Practical classes: 2h</td>
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<tr>
<td>Laboratory classes: 2h</td>
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</tbody>
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### Description:
- Half-wave rectifiers.
- Full-wave rectifiers.
- Parallel and series connection of rectifiers.

### Laboratory sessions
- Practical P2. Diode rectifier.

### Specific objectives:
- A better understanding of the basic concepts of diode rectification.
- An understanding of rectifier's behaviour under different loads.
- An understanding of voltages and currents in rectifiers.
- Familiarity with various rectification topologies.

## TOPIC 4: CONTROLLED RECTIFIERS

<table>
<thead>
<tr>
<th>Learning time: 10h</th>
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</thead>
<tbody>
<tr>
<td>Theory classes: 6h</td>
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<tr>
<td>Practical classes: 2h</td>
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<tr>
<td>Laboratory classes: 2h</td>
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### Description:
- Full-wave rectifiers.
- Symmetrical and asymmetrical triggering.
- Switching control by means of bridge rectifiers.
- Use of controlled rectifiers in DC transmission systems.

### Laboratory sessions
- Practical P3. Thyristor rectifiers.

### Specific objectives:
- An understanding of the operation of controlled rectifiers.
- The ability to analyse a system's voltages and currents under generic triggering conditions.
- An understanding of the operation of controlled rectifiers in rectification mode and inversion mode when used to control a motor.
**TOPIC 5: DC-DC POWER CONVERTERS**

<table>
<thead>
<tr>
<th>Description:</th>
<th>Specific objectives:</th>
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<tbody>
<tr>
<td>- Basic concepts of power converters.</td>
<td>- An understanding of the concept of PWM in switch-based converters.</td>
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<tr>
<td>- Buck DC-DC converters.</td>
<td>- Familiarity with and ability to analyse the most common topologies of single-switch converters.</td>
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<tr>
<td>- Boost DC-DC converters.</td>
<td>- An understanding of and ability to analyse certain advanced topologies.</td>
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<tr>
<td>- Buck-boost DC-DC converters.</td>
<td>- Familiarity with and ability to analyse the operation and performance of full-bridge power converters.</td>
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<td>- Cuk and SEPIC converters.</td>
<td>- Familiarity with the main applications of DC-DC converters.</td>
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<td>- Converters with galvanic isolation.</td>
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<tr>
<td>- Full-bridge DC-DC converters.</td>
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<tr>
<td>- Main applications.</td>
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<td>Laboratory sessions</td>
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<tr>
<td>Practical P4. DC-DC converter.</td>
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**Learning time:** 15h
- Theory classes: 6h
- Practical classes: 5h
- Laboratory classes: 4h
TOPIC 6: DC-AC POWER CONVERTERS

Learning time: 16h
- Theory classes: 6h
- Practical classes: 6h
- Laboratory classes: 4h

Description:
- Basic concepts of sinusoidal pulse-width modulation (PWM).
- Full-bridge single-phase inverters.
- Full-bridge three-phase inverters.
- Modulation schemes (ZSS-PWM, SVM, DPWM).
- Brief introduction to multi-level and resonant converters.
- Applications of DC-AC power inverters.

Laboratory sessions
Practical P5. DC-AC converter.

Specific objectives:
- An understanding of the synthesis of alternating signals using PWM.
- Familiarity with and ability to analyse single-phase and three-phase DC-AC converters.
- The ability to analyse signals generated using the most common modulation techniques.
- An understanding of and ability to design advanced modulation schemes.
- Familiarity with the operating principles of another type of DC-AC converters.
- Familiarity with the main applications of DC-AC converters.

Qualification system

First mid-semester examination - 35%
Second mid-semester examination - 35%
Project plus laboratory work - 30%

For those students who meet the requirements and submit to the reevaluation examination, the grade of the reevaluation exam will replace the grades of all the on-site written evaluation acts (tests, midterm and final exams) and the grades obtained during the course for lab practices, works, projects and presentations will be kept. If the final grade after reevaluation is lower than 5.0, it will replace the initial one only if it is higher. If the final grade after reevaluation is greater or equal to 5.0, the final grade of the subject will be pass 5.0.
Bibliography

Basic:


Complementary:


