340124 - ELAN-K5O10 - Analogue Electronics

Coordinating unit: 340 - EPSEVG - Vilanova i la Geltrú School of Engineering
Teaching unit: 710 - EEL - Department of Electronic Engineering
Academic year: 2019
Degree: BACHELOR’S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING (Syllabus 2009). (Teaching unit Compulsory)
BACHELOR’S DEGREE IN ELECTRICAL ENGINEERING (Syllabus 2009). (Teaching unit Optional)
BACHELOR’S DEGREE IN MECHANICAL ENGINEERING (Syllabus 2009). (Teaching unit Optional)
ECTS credits: 6
Teaching languages: Catalan, Spanish

Teaching staff
Coordinator: PEDRO FRANCISCO GAYA SUÑER
Others: PEDRO FRANCISCO GAYA SUÑER

Prior skills
It is necessary to have a certain knowledge about both discrete and integrated analog devices (diodes, transistors and operational amplifiers).
It is also recommended to have advanced skills on electrical circuits analysis and the use of circuit simulation software.

Requirements
Sistemes Electrònics (SIEK)

Degree competences to which the subject contributes

Specific:
1. CE20. Fundamental knowledge and application of analogue electronics.
2. CE24. Ability to design electronical, analog, digital and power systems.
3. CE25. Knowledge and ability of systems modeling and simulation.

Transversal:
4. SELF-DIRECTED LEARNING - Level 2: Completing set tasks based on the guidelines set by lecturers. Devoting the time needed to complete each task, including personal contributions and expanding on the recommended information sources.
5. TEAMWORK - Level 2. Contributing to the consolidation of a team by planning targets and working efficiently to favor communication, task assignment and cohesion.

Teaching methodology
The methodology is structured in both theory classes and laboratory classes, which include lectures and laboratory activities.

Learning objectives of the subject
At the end of the course, the student should be able to:
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- determine the normal behaviour of an analog circuit.
- design the components of an electronic system so that it develops an analog function to accomplish some specifications.
- use electronic simulation software to understand the behaviour of analog circuits and design component values.
- mount, verify and test analog circuits.

## Study load

<table>
<thead>
<tr>
<th></th>
<th>Total learning time: 150h</th>
<th>Hours large group:</th>
<th>45h</th>
<th>30.00%</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Hours medium group:</td>
<td>0h</td>
<td>0.00%</td>
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<tr>
<td></td>
<td></td>
<td>Hours small group:</td>
<td>15h</td>
<td>10.00%</td>
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<tr>
<td></td>
<td></td>
<td>Guided activities:</td>
<td>0h</td>
<td>0.00%</td>
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<tr>
<td></td>
<td></td>
<td>Self study:</td>
<td>90h</td>
<td>60.00%</td>
</tr>
</tbody>
</table>

## Content

### Learning time: 60h

- **Theory classes:** 18h
- **Laboratory classes:** 6h
- **Self study:** 36h

**Description:**

### Learning time: 51h

- **Theory classes:** 15h
- **Laboratory classes:** 6h
- **Self study:** 30h

**Description:**

### Learning time: 39h

- **Theory classes:** 12h
- **Self study:** 27h

**Description:**
**Qualification system**

The course includes regular assignments consisting in activities in the laboratory (NLab), and two exams (NEx1 and NEx2).

The final grade (NF) is calculated by means of the following expression:

\[
NF = 0.4 \times NEx1 + 0.4 \times NEx2 + 0.2 \times NLab
\]

**Regulations for carrying out activities**

The exams will be individually developed by the student.

All the lab activities must be done to pass the course. A follow-up will be taken into account as part of the student evaluation in his lab activities.

**Bibliography**

**Basic:**