820222 - EAEIA - Analogue Electronics

Coordinating unit: 295 - EEBE - Barcelona East School of Engineering  
Teaching unit: 710 - EEL - Department of Electronic Engineering  
Academic year: 2018  
Degree: BACHELOR'S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING (Syllabus 2009). (Teaching unit Compulsory)  
BACHELOR'S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING (Syllabus 2009). (Teaching unit Compulsory)  
ECTS credits: 6  
Teaching languages: Catalan, Spanish, English

Teaching staff

Coordinator: HERMINIO MARTÍNEZ GARCÍA.  
Others: HERMINIO MARTÍNEZ GARCÍA y otros a determinar al inicio del cuatrimestre.

Opening hours

Timetable: To determine at the semester beginning. It will be announced to the whole students the first week of the course.

Prior skills

The skills acquired in the following courses of the Bachelor's Degree in Industrial Electronics and Automation Engineering (EIA):

- Electronic Systems (STI - 820017).
- Circuit Theory and Electrical Machines (TCME - 820225).
- Electronics Technology (TE-EIA - 820231).

Requirements

PRE-REQUISITES:
As PRE-REQUISITES, it is mandatory to have completed (attended and passed) the following courses of the Bachelor's Degree in Industrial Electronics and Automation Engineering (EIA):

- Electronic Systems (STI - 820017).
- Circuit Theory and Electrical Machines (TCME - 820225).

CO-REQUISITES:
As CO-REQUISITES, it is mandatory to be enrolled (or to have attended previously) the following courses of the Bachelor's Degree in Industrial Electronics and Automation Engineering (EIA):

- Electronic Technology (TE-EIA - 820231).
- Automatic Control (REGA - 820228).
820222 - EAEIA - Analogue Electronics

**Degree competences to which the subject contributes**

**Specific:**
1. Design analogue, digital and power systems.
2. Understand the applications of electronic instrumentation.
3. Understand the fundamentals and applications of analogue electronics.

**Transversal:**
10. SELF-DIRECTED LEARNING - Level 3. Applying the knowledge gained in completing a task according to its relevance and importance. Deciding how to carry out a task, the amount of time to be devoted to it and the most suitable information sources.

**Teaching methodology**

Two theory classes per week with a total of 3.0 h/week, which encompass matter of theory and problems, and 1 h/set. of laboratory classes, grouped into fortnightly sessions.

Additionally, throughout the semester, different classes will be held (schedule will be announced at the beginning of term) with the whole group or part thereof in order to explain, develop and assess cross (generic) competences assigned to the subject.

The course uses:

- Lecture methodology by 40%.
- Individual work by 30%.
- Work in groups by 30%.

The student will develop, in groups of, at most, 2 students, a project of the course design, simulation, and/or implementation related to the content of the course.

**Learning objectives of the subject**

1. To know the different types, internal structures, operation and application circuits of commercial devices and integrated circuits for analog signal processing (VFOAs, OTAs, CCIs, CCIIs, CFOAs, etc.).
2. To know the main electrical characteristics and practical limitations (static and dynamic) of the main commercial integrated circuits for analog signal processing.
3. To understand the concept of 'saturated state' and 'non-saturated state', 'linear electronic function' and 'non-linear electronic function'.
4. To know how to analyze, design, simulate, and implement basic and advanced analog systems that implement linear and nonlinear generation of waveforms and sinusoidal oscillators, regulators and voltage references, etc.
5. To know the synthesis of analog and digital filtering structures, the principle of switched capacitor circuits (SC), and the classification and types of SC circuits.
6. To develop skills in simulation techniques and experimental measurement in analog electronic circuits and systems.
8. To use English in advanced information search (manufacturers datasheets, IEEE journal articles, etc.), regarding the presentation of written and oral work in the course.
## Study load

<table>
<thead>
<tr>
<th>Total learning time: 150h</th>
<th>Hours large group: 45h 30.00%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hours medium group: 0h 0.00%</td>
<td></td>
</tr>
<tr>
<td>Hours small group: 15h 10.00%</td>
<td></td>
</tr>
<tr>
<td>Guided activities: 0h 0.00%</td>
<td></td>
</tr>
<tr>
<td>Self study: 90h 60.00%</td>
<td></td>
</tr>
<tr>
<td>Content</td>
<td>Learning time: 10h</td>
</tr>
<tr>
<td>------------------------------------------------------------------------</td>
<td>---------------------</td>
</tr>
<tr>
<td>1.- Analysis and Design of Operational Amplifier</td>
<td></td>
</tr>
<tr>
<td>Internal Structures.</td>
<td>Theory classes: 5h</td>
</tr>
<tr>
<td></td>
<td>Self study: 5h</td>
</tr>
<tr>
<td>Description:</td>
<td></td>
</tr>
<tr>
<td>Please, see the Spanish or Catalan version.</td>
<td></td>
</tr>
<tr>
<td>Specific objectives:</td>
<td></td>
</tr>
<tr>
<td>Please, see the Spanish or Catalan version of the contents in order</td>
<td></td>
</tr>
<tr>
<td>to see the detailed course syllabus.</td>
<td></td>
</tr>
</tbody>
</table>

| 2.- Practical Limitations of Analog Integrated Circuits.               | Learning time: 8h   |
|                                                                        | Theory classes: 3h  |
|                                                                        | Self study: 5h      |
| Description:                                                           |                     |
| Please, see the Spanish or Catalan version of the contents in order    |                     |
| to see the detailed course syllabus.                                   |                     |
| Specific objectives:                                                   |                     |
| Please, see the Spanish or Catalan version of the contents in order    |                     |
| to see the detailed course syllabus.                                   |                     |

| 3.- Alternatives to VFOA: Commercial Analog Integrated Circuits (OTA, | Learning time: 8h   |
| CCI, CCII, CFOA, etc.). Types, Characteristics, and Classification.   | Theory classes: 3h  |
|                                                                        | Self study: 5h      |
| Description:                                                           |                     |
| Please, see the Spanish or Catalan version of the contents in order    |                     |
| to see the detailed course syllabus.                                   |                     |
| Specific objectives:                                                   |                     |
| Please, see the Spanish or Catalan version of the contents in order    |                     |
| to see the detailed course syllabus.                                   |                     |
### 4.- Implementation of Linear and Non-Linear Electronic Functions by Means of Analog Integrated Circuits Operating in Non-Saturation and Saturation Zones.

**Learning time:** 12h 30m  
- Theory classes: 3h 30m  
- Laboratory classes: 4h  
- Self study: 5h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

### 5.- Frequency Response of Transistor Amplifiers.

**Learning time:** 14h  
- Theory classes: 4h  
- Self study: 10h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

### 6.- Transistor and VFOA Feedback Amplifiers: Effects of the Negative and Positive Feedback.

**Learning time:** 8h  
- Theory classes: 3h  
- Self study: 5h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.
### 7.- Time Circuits, Signal Generation and Sinusoidal Oscillators.

**Learning time:** 18h  
Theory classes: 4h  
Laboratory classes: 4h  
Self study: 10h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

### 8.- Continuous and Discrete Transfer Functions: 's' and 'z' Transforms for Electrical Filter.

**Learning time:** 8h 30m  
Theory classes: 3h 30m  
Self study: 5h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

### 9.- Continuous-Time and Digital Active Filtering Cells. Design and Analysis of High-Order Filters by Means of Polynomials, Tables, and Software.

**Learning time:** 19h  
Theory classes: 5h  
Laboratory classes: 4h  
Self study: 10h

**Description:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**  
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.
### 10.- Switched-Capacitor Circuits (SCC) and Programmable Analog Devices (FPAAs).

**Description:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

<table>
<thead>
<tr>
<th>Learning time:</th>
<th>11h</th>
</tr>
</thead>
<tbody>
<tr>
<td>Theory classes:</td>
<td>3h</td>
</tr>
<tr>
<td>Laboratory classes:</td>
<td>3h</td>
</tr>
<tr>
<td>Self study:</td>
<td>5h</td>
</tr>
</tbody>
</table>

### 11.- Power Amplification: Output Stages.

**Description:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

<table>
<thead>
<tr>
<th>Learning time:</th>
<th>13h</th>
</tr>
</thead>
<tbody>
<tr>
<td>Theory classes:</td>
<td>3h</td>
</tr>
<tr>
<td>Self study:</td>
<td>10h</td>
</tr>
</tbody>
</table>

### 12.- Current and Voltage Regulators and References.

**Description:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

**Specific objectives:**
Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

<table>
<thead>
<tr>
<th>Learning time:</th>
<th>13h</th>
</tr>
</thead>
<tbody>
<tr>
<td>Theory classes:</td>
<td>3h</td>
</tr>
<tr>
<td>Self study:</td>
<td>10h</td>
</tr>
</tbody>
</table>
The grade or scoring of the course will be carried out according to:

- 1 or 2 midterm exams: 20%.
- Final Exam: 40%.
- Course project (project to design, simulate, and implement analog electronic systems): 20%.
- Laboratory activities and tests: 20%.

All these tasks will also serve to assess the cross (generic) competences assigned to the course.

This course does not have re-assessment test ("prova de reavaluació").

Qualification system

The implementation of the different tests consists of:

- Midterm exams: written tests, theoretical or sizing problems of solar energy testing, and analysis and/or synthesis (design) of analog electronic systems.
- Final exam: written, theoretical and/or sizing problems of solar energy test, and analysis and synthesis (design) of analog electronic systems.
- Course project: The course project will involve conducting course design work, sizing and/or simulation related to the contents of the subject.
- Activities, testing and laboratory experiments: Laboratory experiences and activities on Electronics Engineering.

Thanks to all these tasks, the cross (generic) competences assigned to the course will be also evaluated.
Bibliography

Basic:


Complementary:


Others resources:

Please, see the Spanish or Catalan version of the contents in order to see the detailed course syllabus.

Hyperlink

Moodle ATENEA: http://atenea.upc.edu/moodle/