820224 - ELDI - Digital Electronics

Coordinating unit: 295 - EEBE - Barcelona East School of Engineering
Teaching unit: 710 - EEL - Department of Electronic Engineering
Academic year: 2018
Degree: BACHELOR'S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING
(Syllabus 2009). (Teaching unit Compulsory)
BACHELOR'S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING
(Syllabus 2009). (Teaching unit Compulsory)
ECTS credits: 6  Teaching languages: Catalan

Teaching staff

Coordinator: Cosp Vilella, Jordi
Gutiérrez Escribá, Fèlix

Prior skills

Basic knowledge of digital electronics.
Oral and written expression. Level 2

Requirements

Having completed the course: Electronic Systems

Degree competences to which the subject contributes

Specific:
2. Understand the fundamentals and applications of digital electronics and microprocessors.
1. Design analogue, digital and power systems.

Transversal:
3. EFFICIENT ORAL AND WRITTEN COMMUNICATION - Level 3. Communicating clearly and efficiently in oral and written presentations. Adapting to audiences and communication aims by using suitable strategies and means.

Teaching methodology

The methodology is based in lectures and exercises that are proposed regularly to be done in the classroom and out of it. Also, laboratory exercises are done every two weeks at the laboratories of the subject.

Learning objectives of the subject

Acquire the fundamental concepts of digital circuit design tools and platforms available to perform them.
820224 - ELDI - Digital Electronics

**Study load**

<table>
<thead>
<tr>
<th>Total learning time: 150h</th>
<th>Hours large group:</th>
<th>45h</th>
<th>30.00%</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Hours medium group:</td>
<td>0h</td>
<td>0.00%</td>
</tr>
<tr>
<td></td>
<td>Hours small group:</td>
<td>15h</td>
<td>10.00%</td>
</tr>
<tr>
<td></td>
<td>Guided activities:</td>
<td>0h</td>
<td>0.00%</td>
</tr>
<tr>
<td></td>
<td>Self study:</td>
<td>90h</td>
<td>60.00%</td>
</tr>
</tbody>
</table>
## Content

<table>
<thead>
<tr>
<th>Section</th>
<th>Learning time:</th>
<th>Description:</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Introduction and review of previous concepts</strong></td>
<td>7h 30m</td>
<td>Brief overview of the knowledge acquired in the course Electronic Systems.</td>
</tr>
<tr>
<td><strong>High Level Hardware Design Basics</strong></td>
<td>11h 15m</td>
<td>Introduction and basics of digital electronic system design using high-level descriptions and programmable logic devices.</td>
</tr>
<tr>
<td><strong>Combinational blocks</strong></td>
<td>16h 15m</td>
<td>Description, operation and use of common combinational blocks.</td>
</tr>
<tr>
<td><strong>Sequential blocks</strong></td>
<td>16h 15m</td>
<td>Description, operation and use of basic common sequential blocks.</td>
</tr>
</tbody>
</table>
## Sequential Systems

**Learning time:** 16h 15m  
Theory classes: 4h 30m  
Laboratory classes: 2h  
Self study: 9h 45m

**Description:**  
Analysis and design of sequential systems of medium complexity.

## Finite State Machines (FSM)

**Learning time:** 18h 30m  
Theory classes: 4h 30m  
Laboratory classes: 2h  
Self study: 12h

**Description:**  
Analysis and Design of Finite State Machines

## Arithmetic Systems and Binary Arithmethics

**Learning time:** 7h 30m  
Theory classes: 3h  
Self study: 4h 30m

**Description:**  
Description and use of the numerical systems used in digital electronics. Analysis and design of arithmetic blocks.

## Electrical characteristics

**Learning time:** 12h 30m  
Theory classes: 3h  
Laboratory classes: 2h  
Self study: 7h 30m

**Description:**  
Description of voltage levels and delays of logic gates and digital blocks. Calculation of limit working electrical conditions.

## Algorithmic State Machines (ASM)

**Learning time:** 15h  
Theory classes: 6h  
Self study: 9h

**Description:**  
Analysis and Design of Algorithmic State Machines. The datapath and the control unit.
End mark = 0.20*(lab)+0.20*(homework exercises)+0.20*(mid-term test/s)+0.40*(final exam)

There is no re-assessment exam in this course.

Qualification system

Regulations for carrying out activities

Is mandatory to have completed the lab exercises and bring ID or other identification on the day of the periodic controls and final exam.
Bibliography

Basic:


Complementary:


