# 820224 - ELDI - Digital Electronics

**Coordinating unit:** 295 - EEBE - Barcelona East School of Engineering  
**Teaching unit:** 710 - EEL - Department of Electronic Engineering  
**Academic year:** 2019  
**Degree:** BACHELOR'S DEGREE IN INDUSTRIAL ELECTRONICS AND AUTOMATIC CONTROL ENGINEERING (Syllabus 2009). (Teaching unit Compulsory)  
**ECTS credits:** 6  
**Teaching languages:** Catalan

### Teaching staff

**Coordinator:** FÉLIX GUTIÉRREZ ESCRIBÁ - JORDI COSP VILELLA  
**Others:**  
**Primer quadrimestre:**  
MAZIAR AHMADI ZEIDABADI - M13, M14  
JORDI COSP VILELLA - M11, M12, M13, M14, M21, M22, M23  
FÉLIX GUTIÉRREZ ESCRIBÁ - M11, M12, M21, M22, M23  
**Segon quadrimestre:**  
MAZIAR AHMADI ZEIDABADI - T11, T12, T13, T14  
FÉLIX GUTIÉRREZ ESCRIBÁ - T11, T12, T13, T14

### Prior skills

Basic knowledge of digital electronics.  
Oral and written expression. Level 2

### Requirements

SISTEMES ELECTRÒNICS - Prerequisite

### Degree competences to which the subject contributes

#### Specific:

1. Design analogue, digital and power systems.

#### Transversal:

3. EFFICIENT ORAL AND WRITTEN COMMUNICATION - Level 3. Communicating clearly and efficiently in oral and written presentations. Adapting to audiences and communication aims by using suitable strategies and means.

### Teaching methodology

The methodology is based on lectures and exercises that are proposed regularly to be done in the classroom and out of it. Also, laboratory exercises are done every two weeks at the laboratories of the subject.

### Learning objectives of the subject

Acquire the fundamental concepts of digital circuit design tools and platforms available to perform them.
# Study load

<table>
<thead>
<tr>
<th></th>
<th>Hours large group:</th>
<th>Hours medium group:</th>
<th>Hours small group:</th>
<th>Guided activities:</th>
<th>Self study:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Total learning time:</td>
<td>45h</td>
<td>0h</td>
<td>15h</td>
<td>0h</td>
<td>90h</td>
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<tr>
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<td>30.00%</td>
<td>0.00%</td>
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</tbody>
</table>
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## Content

<table>
<thead>
<tr>
<th>Section</th>
<th>Learning time</th>
<th>Theory classes:</th>
<th>Laboratory classes:</th>
<th>Self study:</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Introduction and review of previous concepts</strong></td>
<td>7h 30m</td>
<td>3h</td>
<td>0h</td>
<td>4h 30m</td>
</tr>
<tr>
<td>Description:</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Brief overview of the knowledge acquired in the course Electronic Systems</td>
<td></td>
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<tr>
<td><strong>High Level Hardware Design Basics</strong></td>
<td>11h 15m</td>
<td>1h 30m</td>
<td>3h</td>
<td>6h 45m</td>
</tr>
<tr>
<td>Description:</td>
<td></td>
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</tr>
<tr>
<td>Introduction and basics of digital electronic system design using high-level descriptions and programmable logic devices</td>
<td></td>
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<tr>
<td><strong>Combinational blocks</strong></td>
<td>16h 15m</td>
<td>4h 30m</td>
<td>2h</td>
<td>9h 45m</td>
</tr>
<tr>
<td>Description:</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Description, operation and use of common combinational blocks.</td>
<td></td>
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<tr>
<td><strong>Sequential blocks</strong></td>
<td>16h 15m</td>
<td>4h 30m</td>
<td>2h</td>
<td>9h 45m</td>
</tr>
<tr>
<td>Description:</td>
<td></td>
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<tr>
<td>Description, operation and use of basic common sequential blocks.</td>
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</tbody>
</table>
## Sequential Systems

**Description:**
Analysis and design of sequential systems of medium complexity.

**Learning time:** 16h 15m
- Theory classes: 4h 30m
- Laboratory classes: 2h
- Self study: 9h 45m

## Finite State Machines (FSM)

**Description:**
Analysis and Design of Finite State Machines

**Learning time:** 18h 30m
- Theory classes: 4h 30m
- Laboratory classes: 2h
- Self study: 12h

## Arithmetic Systems and Binary Arithmethics

**Description:**
Description and use of the numerical systems used in digital electronics. Analysis and design of arithmetic blocks.

**Learning time:** 7h 30m
- Theory classes: 3h
- Self study: 4h 30m

## Electrical characteristics

**Description:**
Description of voltage levels and delays of logic gates and digital blocks. Calculation of limit working electrical conditions.

**Learning time:** 12h 30m
- Theory classes: 3h
- Laboratory classes: 2h
- Self study: 7h 30m

## Algorithmic State Machines (ASM)

**Description:**
Analysis and Design of Algorithmic State Machines. The datapath and the control unit.

**Learning time:** 15h
- Theory classes: 6h
- Self study: 9h
Memories and Programmable Logic Devices

<table>
<thead>
<tr>
<th>Learning time: 12h 30m</th>
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<tbody>
<tr>
<td>Theory classes: 3h</td>
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<tr>
<td>Laboratory classes: 2h</td>
</tr>
<tr>
<td>Self study: 7h 30m</td>
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Description:
Description and use of the most common logic memories. Overview of different possible realizations of digital circuits using the various existing commercial programmable devices (CPLD and FPGA).

The Microprocessor

<table>
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<th>Learning time: 15h</th>
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<tr>
<td>Theory classes: 6h</td>
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<tr>
<td>Self study: 9h</td>
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Description:
Introduction to the microprocessor system and its internal architecture.

Qualification system

End mark = 0.20* (lab) + 0.20* (homework exercises) + 0.20* (mid-term test/s) + 0.40* (final exam)
There is no re-assessment exam in this course.

Regulations for carrying out activities

It is mandatory to have completed the lab exercises and bring ID or other identification on the day of the periodic controls and final exam.
Bibliography

Basic:


Complementary:


