230675 - EDIS - Edison: Energy Management for Distributed and Integrated Systems

Degree competences to which the subject contributes

Learning objectives of the subject:
The aim of this course is to introduce the students in several techniques of modelling, design and control of energy management architectures, particularly in an IC context, designing its subsystems and related modulation, control and management policies. The course focuses on energy management and supply subsystems specifically targeting communication and computing applications.

Requisites: Students coming from academic studies other than B. Sc. Electronics Systems Engineering or equivalent ones, should have successfully passed the examinations of the bridging courses "Control" and "POT".

Learning results of the subject:

- Ability to design energy management architectures, particularly in an IC context
- Ability to design energy management subsystems, including circuit and model aspects
- Ability to understand and apply energy management architectures for distributed and integrated applications
- Ability to understand and apply energy management subsystems, particularly in an IC context
- Ability to understand and apply modulations, control and energy management policies
- Ability to design integrated and distributed energy management systems in various ICT applications

Teaching methodology:
- Lectures
- Exercises
- Other activities
- Extended answer test (Final Exam)
### Study load

<table>
<thead>
<tr>
<th></th>
<th>Hours large group:</th>
<th>%</th>
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<tbody>
<tr>
<td>Total learning time:</td>
<td>26h</td>
<td>20.80%</td>
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<tr>
<td>Hours medium group:</td>
<td>0h</td>
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<tr>
<td>Hours small group:</td>
<td>13h</td>
<td>10.40%</td>
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<tr>
<td>Guided activities:</td>
<td>0h</td>
<td>0.00%</td>
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<tr>
<td>Self study:</td>
<td>86h</td>
<td>68.80%</td>
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## 1. Introduction to energy management

**Learning time:** 7h  
Theory classes: 2h  
Self study: 5h

**Description:**  
- Basic concepts. Energy processing vs signal processing  
- Energy processing architecture: source, processor and load  
- Current applications

## 2. Efficient energy conversion subsystems

**Learning time:** 21h  
Theory classes: 6h  
Self study: 15h

**Description:**  
- Converter classification: linear converters, switched capacitor converters, switching power converters  
- Switching power converters: fundamentals of synthesis and design-oriented analysis  
- Switching power regulators  
- Power processing modular architectures

## 3. Batteries and other energy sources

**Learning time:** 13h  
Theory classes: 4h  
Self study: 9h

**Description:**  
- Classification of batteries  
- Battery modelling  
- Other energy sources: Fuel cells, supercapacitors, photovoltaic cells

## 4. Energy management in battery-operated mobile telephone portable terminals

**Learning time:** 20h  
Theory classes: 6h  
Self study: 14h

**Description:**  
- Energy management within the system-on-chip architecture  
- Power converter miniaturization guidelines  
- Improved efficiency techniques: adaptive power management for DSP and RF amplifiers  
- On-chip energy distribution networks
## 5. Powering microprocessors

**Learning time:** 7h  
Theory classes: 2h  
Self study: 5h

**Description:**  
- Voltage regulator modules (VRM). Specifications.  
- Decoupling issues  
- Modular powering architectures for multi-processor systems.  
- Other issues: UPS (Uninterruptible power supplies) and PFC (Power factor correction) circuits

## 6. Bus architectures for energy distribution in satellites

**Learning time:** 7h  
Theory classes: 2h  
Self study: 5h

**Description:**  
- Energy management architectures for aerospace applications.  
- Effect of satellite orbit  
- Energy bus classification: non-regulated, hybrid and regulated bus

## 7. Other applications

**Learning time:** 13h  
Theory classes: 4h  
Self study: 9h

**Description:**  
- Techniques for efficient DC to RF power conversion  
- Efficient switching power audio amplifiers  
- Power issues in line drivers  
- Energy Harvesting circuits and systems

## 8. Laboratory 1

**Learning time:** 13h  
Laboratory classes: 5h  
Self study: 8h

**Description:**  
Circuit-level simulation of a voltage regulator module (VRM) powering a microprocessor
### Planning of activities

#### LECTURES

#### EXERCISES

**Description:**
Exercises to strengthen the theoretical knowledge.

#### OTHER ACTIVITIES

**Description:**
Numerical simulation homework

#### EXTENDED ANSWER TEST (FINAL EXAM)

**Description:**
Final examination.

### Qualification system

Final examination: from 60% to 70%
Exercises: from 30% to 40%
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Bibliography

Basic:


Complementary:


